

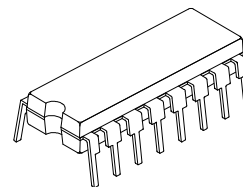
**8-bit Constant Current LED Sink Driver****Features**

- I 8 constant-current output channels
- I Output current adjustable through an external resistor
- I Serial data in/parallel data out
- I Output current: 5-90 mA
- I 20 MHz clock frequency
- I Schmitt trigger input

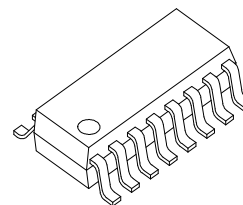
**Product Description**

MBI5001, utilizing the most advanced silicon technology, is targeted for LED panel display. MBI5001 contains CMOS shift registers and latch functions converting serial input data into parallel output format. At the output stage, eight regulated current sources were designed to provide 5-90 mA constant current for driving LEDs.

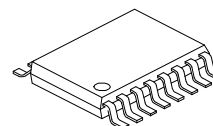
MBI5001 provides users with great flexibility and device performance while using the MBI5001 in their LED panel system design. Users may adjust the output current of the MBI5001 through an external resistor,  $R_{ext}$ , which gives users flexibility in controlling the light intensity of LEDs. MBI5001 guarantees to endure maximum 17V at the output port, allowing users to connect more LEDs in series. The high clock frequency, 20 MHz, also satisfies the system requirement of high volume data transmission.

**MBI5001CN**

DIP16-P-300-2.54 Weight: 1.02g(typ)

**MBI5001CD**

SOP16-P-150-1.27 Weight: 0.13g(typ)

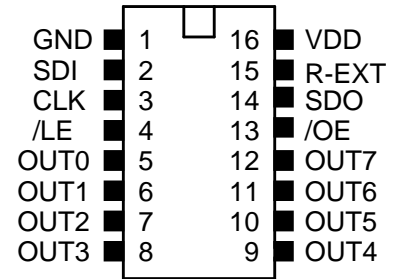
**MBI5001CP**

SSOP16-P-150-0.64 Weight: 0.07g(typ)

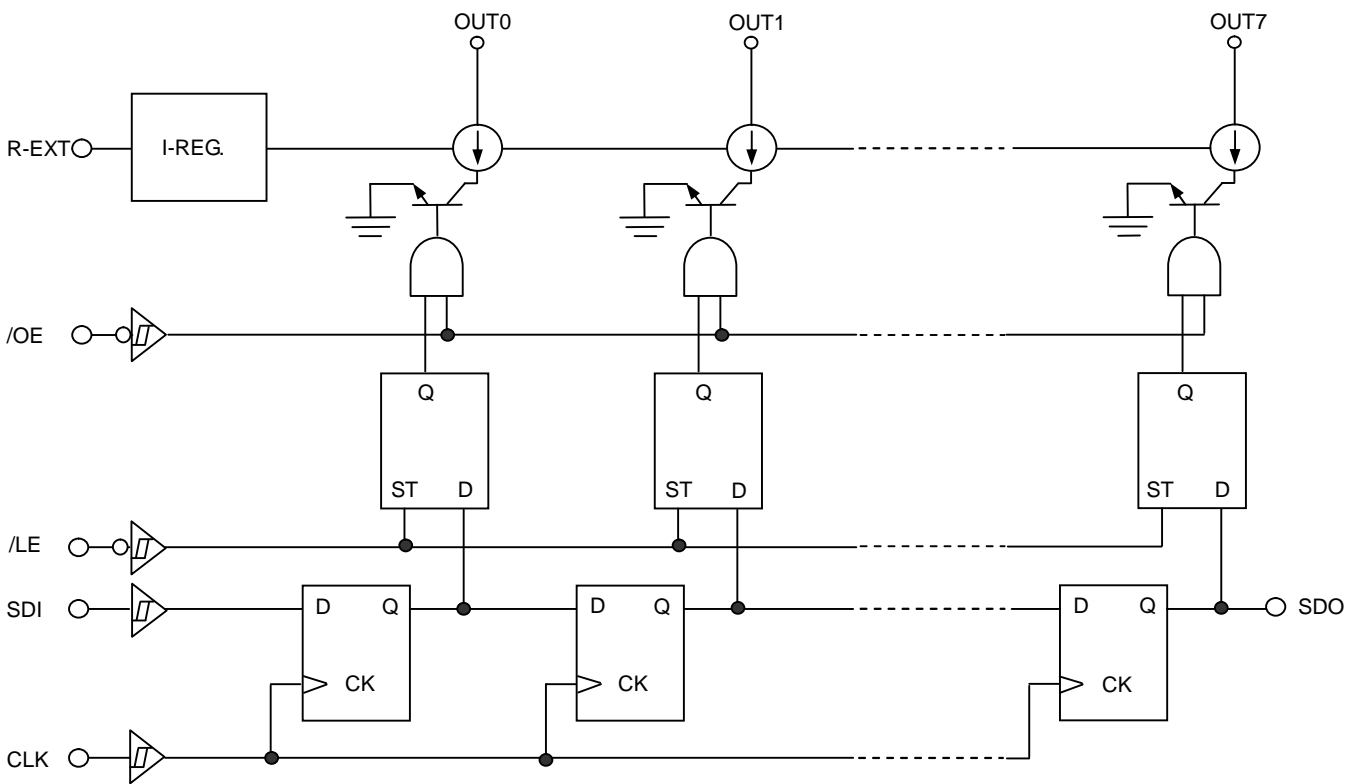
Terminal Description

| PIN NO. | PIN NAME | FUNCTION                     |
|---------|----------|------------------------------|
| 1       | GND      | GND terminal                 |
| 2       | SDI      | Serial data input terminal   |
| 3       | CLK      | Clock input terminal         |
| 4       | /LE      | Latch input terminal         |
| 5-12    | OUT0-7   | Output terminal              |
| 13      | /OE      | Output enable input terminal |
| 14      | SDO      | Serial data out terminal     |
| 15      | R-EXT    | Constant current programming |
| 16      | VDD      | 5V supply voltage terminal   |

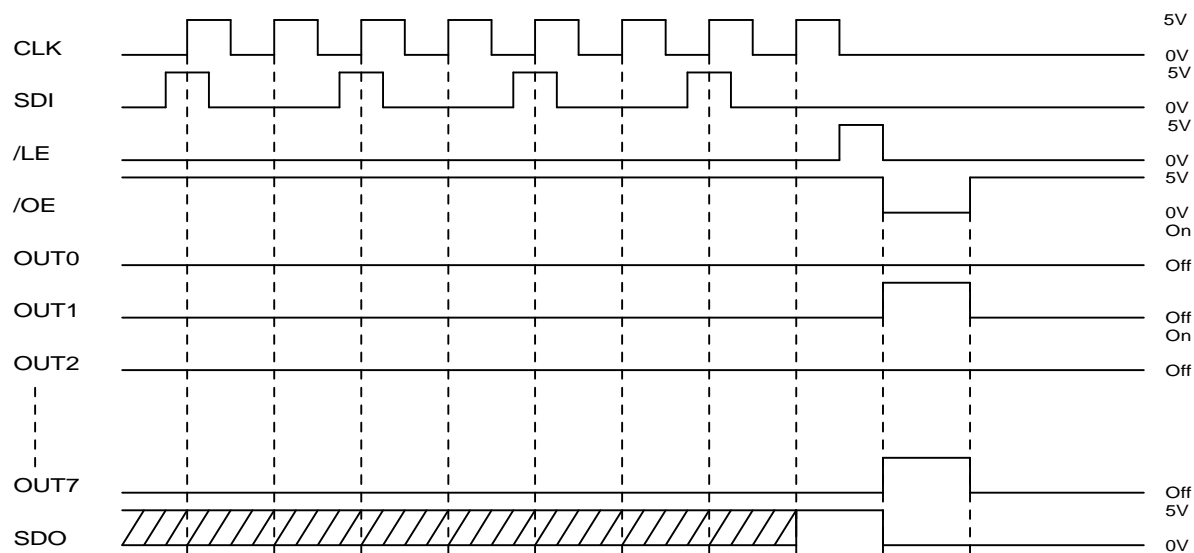
Pin Description



Block Diagram



Timing Diagram

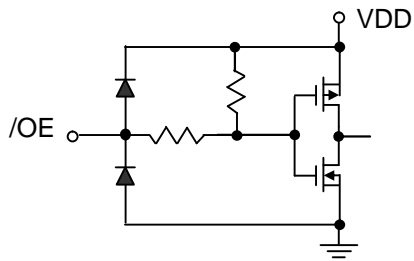


Truth table

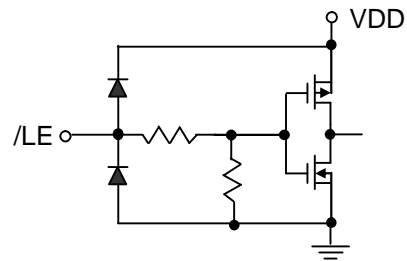
| CLK  | /LE | /OE | SDI       | OUT0      | OUT5      | OUT7      | SDO       |
|------|-----|-----|-----------|-----------|-----------|-----------|-----------|
| UP   | H   | L   | $D_n$     | $D_n$     | $D_{n-5}$ | $D_{n-7}$ | $D_{n-7}$ |
| UP   | L   | L   | $D_{n+1}$ | NO CHANGE |           |           | $D_{n-6}$ |
| UP   | H   | L   | $D_{n+2}$ | $D_{n+2}$ | $D_{n-3}$ | $D_{n-5}$ | $D_{n-5}$ |
| DOWN | X   | L   | $D_{n+3}$ | $D_{n+2}$ | $D_{n-3}$ | $D_{n-5}$ | $D_{n-5}$ |
| DOWN | X   | H   | $D_{n+3}$ | Off       |           |           | $D_{n-5}$ |

Equivalent Circuit of Inputs and Outputs:

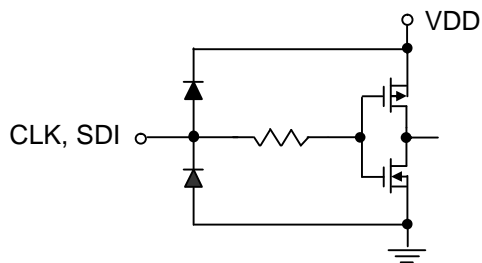
/OE terminal



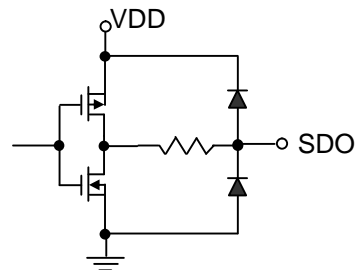
/LE terminal



CLK, SDI terminal



SDO terminal



Maximum Ratings

| CHARACTERISTIC  |           | SYMBOL        | RATING             | UNIT               |
|---|-----------|---------------|--------------------|--------------------|
| Supply Voltage  |           | $V_{DD}$      | 0~+7.0             | V                  |
| Output Voltage  |           | $V_{CE}$      | -0.5~+17.0         | V                  |
| Output Current  |           | $I_{OUT}$     | +90                | mA                 |
| Input Voltage   |           | $V_{IN}$      | -0.4~ $V_{DD}+0.4$ | V                  |
| GND Terminal Current                                    |           | $I_{GND}$     | 720                | mA                 |
| Clock Frequency   |           | $F_{CLK}$     | 20                 | MHz                |
| Power Dissipation<br>(ON PCB, $T_a=25^\circ\text{C}$ )  | CN – type | $P_D$         | 1.64               | W                  |
|   | CD – type |               | 1.06               |                    |
|   | CP – type |               | 0.88               |                    |
| Thermal Resistance<br>(ON PCB, $T_a=25^\circ\text{C}$ ) | CN – type | $R_{th(j-a)}$ | 76                 | $^\circ\text{C/W}$ |
|   | CD – type |               | 117                |                    |
|   | CP – type |               | 141                |                    |
| Operating Temperature                                   |           | $T_{opr}$     | -40~+85            | $^\circ\text{C}$   |
| Storage Temperature                                     |           | $T_{stg}$     | -55~+150           | $^\circ\text{C}$   |

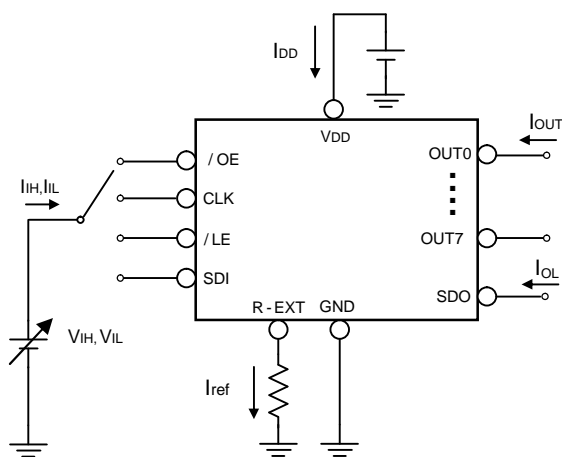
Recommended Operating Condition

| CHARACTERISTIC       |           | SYMBOL       | CONDITION                    | MIN.              | TYP. | MAX.         | UNIT |
|----------------------|-----------|--------------|------------------------------|-------------------|------|--------------|------|
| Supply Voltage       |           | $V_{DD}$     | -                            | 4.5               | 5.0  | 5.5          | V    |
| Output Voltage       |           | $V_{CE}$     | -                            | -                 | -    | 17.0         | V    |
| Output Current       |           | $I_{OUT}$    | DC Test Circuit              | 5                 | -    | 90           | mA   |
|                      |           | $I_{OH}$     | SDO                          | -                 | -    | -1.0         | mA   |
|                      |           | $I_{OL}$     | SDO                          | -                 | -    | 1.0          | mA   |
| Input Voltage        |           | $V_{IH}$     | -                            | $0.7V_{DD}$       | -    | $V_{DD}+0.3$ | V    |
|                      |           | $V_{IL}$     | -                            | -0.3              | -    | $0.3V_{DD}$  | V    |
| /LE Pulse Width      |           | $t_{w(L)}$   | $V_{DD}=4.5\sim 5.5\text{V}$ | 25                | -    | -            | ns   |
| CLK Pulse Width      |           | $t_{w(CLK)}$ |                              | 25                | -    | -            | ns   |
| /OE Pulse Width      |           | $t_{w(OE)}$  |                              | 400               | -    | -            | ns   |
| Setup Time for DATA  |           | $t_{su(D)}$  |                              | 20                | -    | -            | ns   |
| Hold Time for DATA   |           | $t_{h(D)}$   |                              | 15                | -    | -            | ns   |
| Setup Time for LATCH |           | $t_{su(L)}$  |                              | 60                | -    | -            | ns   |
| Hold Time for LATCH  |           | $t_{h(L)}$   |                              | 20                | -    | -            | ns   |
| Clock Frequency      |           | $F_{CLK}$    |                              | Cascade Operation | -    | -            | 20.0 |
| Power Dissipation    | CN – type | $P_D$        | $T_a=85^\circ\text{C}$       | -                 | -    | 0.85         | W    |
|                      | CD – type |              |                              |                   |      | 0.55         |      |
|                      | CP – type |              |                              |                   |      | 0.46         |      |

Electrical Characteristics

| CHARACTERISTIC         |           | SYMBOL          | CONDITION  | MIN.        | TYP.      | MAX.        | UNIT       |
|------------------------|-----------|-----------------|--|-------------|-----------|-------------|------------|
| Input Voltage          | “H” level | $V_{IH}$        | -  | $0.7V_{DD}$ | -         | $V_{DD}$    | V          |
|                        | “L” level | $V_{IL}$        | -  | GND         | -         | $0.3V_{DD}$ |            |
| Output Leakage Current |           | $I_{OH}$        | $V_{OH}=17.0V$   | -           | -         | 10          | $\mu A$    |
| Output Voltage         | SDO       | $V_{OL}$        | $I_{OL}=+1.0mA$  | -           | -         | 0.4         | V          |
|                        |           | $V_{OH}$        | $I_{OH}=-1.0mA$  | 4.6         | -         | -           | V          |
| Output Current 1       |           | $I_{OUT1}$      | $V_{CE}=0.8V$<br>$R_{ext}=865\ \Omega$<br>(include Skew) | -           | 40.0      | -           | mA         |
| Current Skew           |           | $dI_{OUT1}$     | $I_{OUT}=40mA$<br>$V_{CE}=0.8V$<br>$R_{ext}=865\ \Omega$ | -           | $\pm 1.5$ | $\pm 6.0$   | %          |
| Output Current 2       |           | $I_{OUT2}$      | $V_{CE}=1.2V$<br>$R_{ext}=330\ \Omega$<br>(include Skew) | -           | 80.0      | -           | mA         |
| Current Skew           |           | $dI_{OUT2}$     | $I_{OUT}=80mA$<br>$V_{CE}=1.2V$<br>$R_{ext}=330\ \Omega$ | -           | $\pm 1.5$ | $\pm 6.0$   | %          |
| Pull-up Resistor       |           | $R_{IN(up)}$    | -  | 150         | 300       | 600         | K $\Omega$ |
| Pull-down Resistor     |           | $R_{IN(down)}$  | -  | 85          | 200       | 400         | K $\Omega$ |
| Supply Current         | “OFF”     | $I_{DD(off) 1}$ | $R_{ext}=Open, OUT0\sim 7=Off$                           | -           | 0.1       | 1.0         | mA         |
|                        |           | $I_{DD(off) 2}$ | $R_{ext}=865\ \Omega, OUT0\sim 7=Off$                    | 0.1         | 0.2       | 1.0         |            |
|                        |           | $I_{DD(off) 3}$ | $R_{ext}=330\ \Omega, OUT0\sim 7=Off$                    | 0.1         | 0.2       | 1.0         |            |
|                        | “ON”      | $I_{DD(on) 1}$  | $R_{ext}=865\ \Omega, OUT0\sim 7=On$                     | 7.0         | 12.0      | 18.0        |            |
|                        |           | $I_{DD(on) 2}$  | $R_{ext}=330\ \Omega, OUT0\sim 7=On$                     | 10.0        | 22.0      | 32.0        |            |

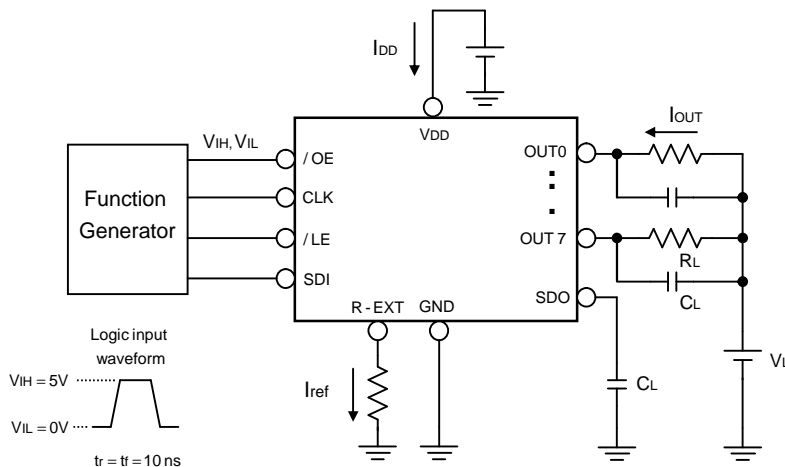
Test Circuit for Electrical Characteristics



Switching Characteristics

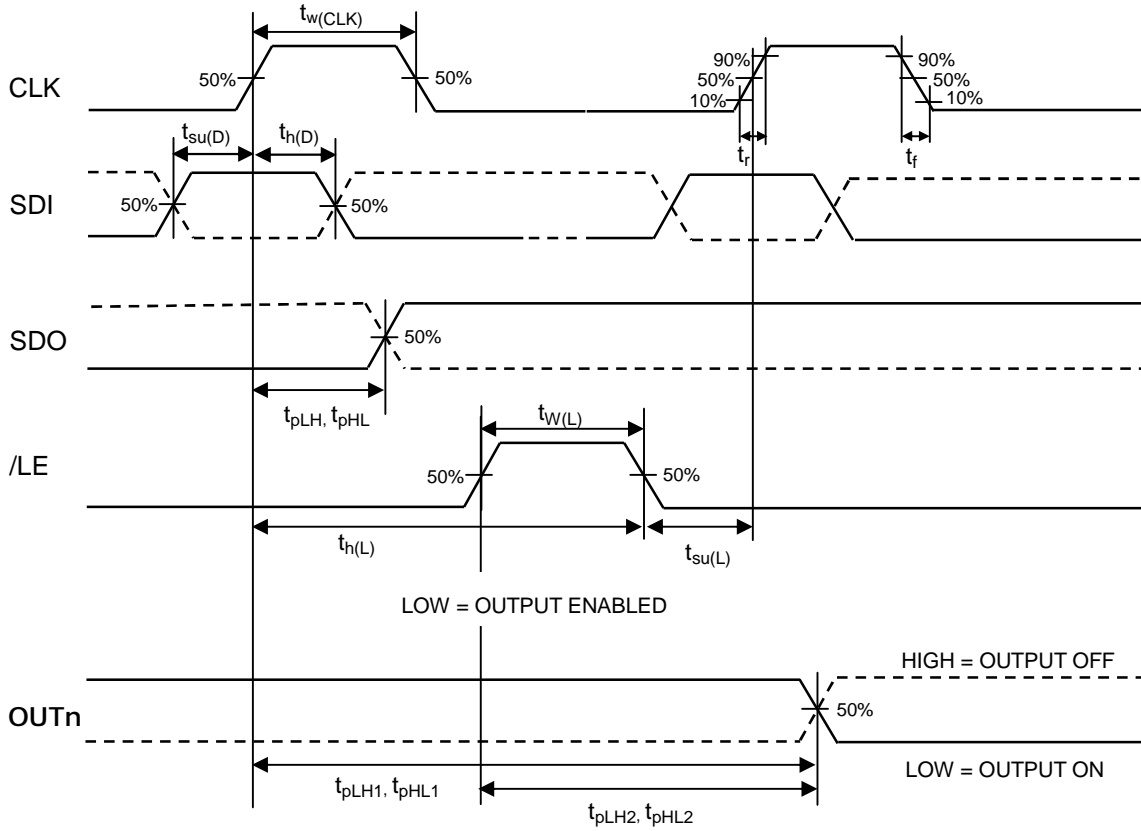
| CHARACTERISTIC                         |          | SYMBOL      | CONDITION  | MIN. | TYP. | MAX. | UNIT |
|--|----------|-------------|--|------|------|------|------|
| Propagation Delay Time<br>("L" to "H") | CLK-OUTn | $t_{pLH1}$  | $V_{DD}=5.0V$<br>$V_{CE}=0.8V$<br>$V_{IH}=V_{DD}$<br>$V_{IL}=GND$<br>$R_{ext}=865\ \Omega$<br>$I_{OUT}=40mA$<br>$V_L=3.4V$<br>$R_L=65\ \Omega$<br>$C_L=10.5pF$ | -    | 200  | 300  | ns   |
|  | /LE-OUTn | $t_{pLH2}$  |  | -    | 200  | 300  | ns   |
|  | /OE-OUTn | $t_{pLH3}$  |  | -    | 200  | 300  | ns   |
|  | CLK-SDO  | $t_{pLH}$   |  | 20   | 50   | 70   | ns   |
| Propagation Delay Time<br>("H" to "L") | CLK-OUTn | $t_{pHL1}$  |  | -    | 200  | 300  | ns   |
|  | /LE-OUTn | $t_{pHL2}$  |  | -    | 200  | 300  | ns   |
|  | /OE-OUTn | $t_{pHL3}$  |  | -    | 200  | 300  | ns   |
|  | CLK-SDO  | $t_{pHL}$   |  | 20   | 50   | 70   | ns   |
| Pulse Width                            | CLK      | $t_w(CLK)$  |  | 15   | -    | -    | ns   |
|  | /LE      | $t_w(L)$    |  | 20   | -    | -    | ns   |
| Set-up Time for LATCH                  |          | $t_{su(L)}$ |  | 10   | -    | -    | ns   |
| Hold Time for LATCH                    |          | $t_{h(L)}$  |  | 10   | -    | -    | ns   |
| Maximum CLK Rise Time                  |          | $t_r$       | -  | -    | 500  | ns   |      |
| Maximum CLK Fall Time                  |          | $t_f$       | -  | -    | 500  | ns   |      |
| Output Rise Time                       |          | $t_{or}$    | -  | 150  | 200  | ns   |      |
| Output Fall Time                       |          | $t_{of}$    | -  | 150  | 200  | ns   |      |

Test Circuit for Switching Characteristics

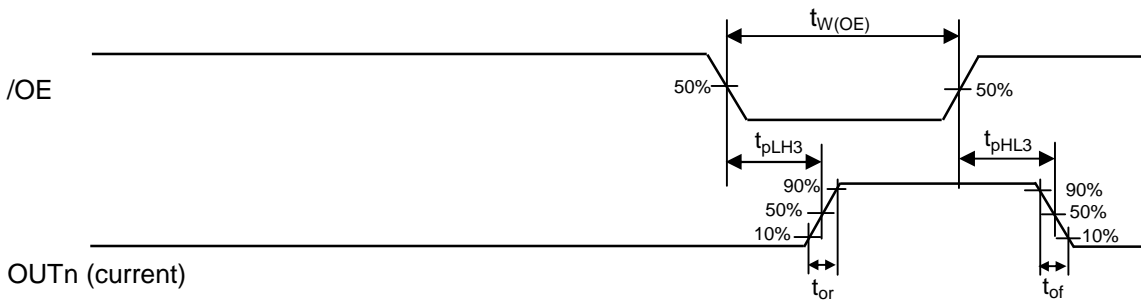


Timing Waveform

1. CLK, SDO, /LE, OUTn

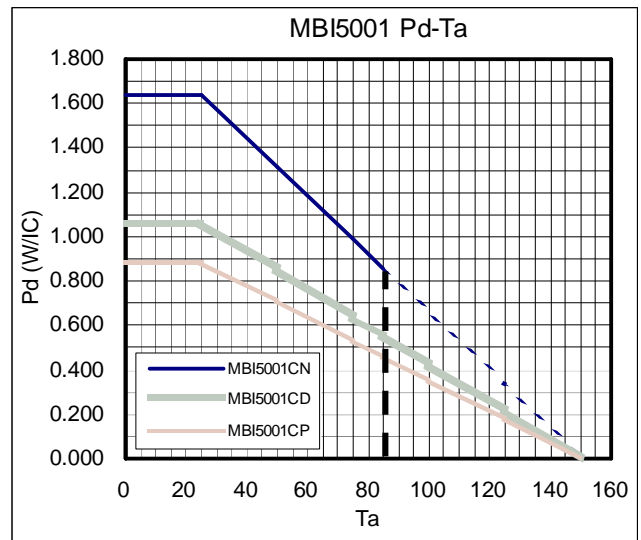
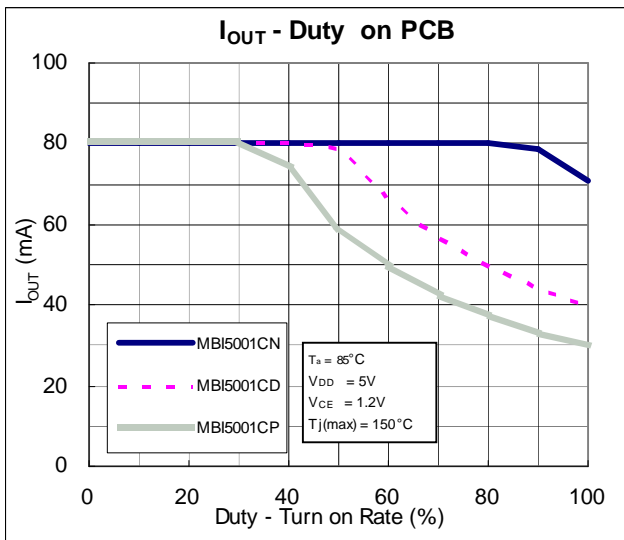
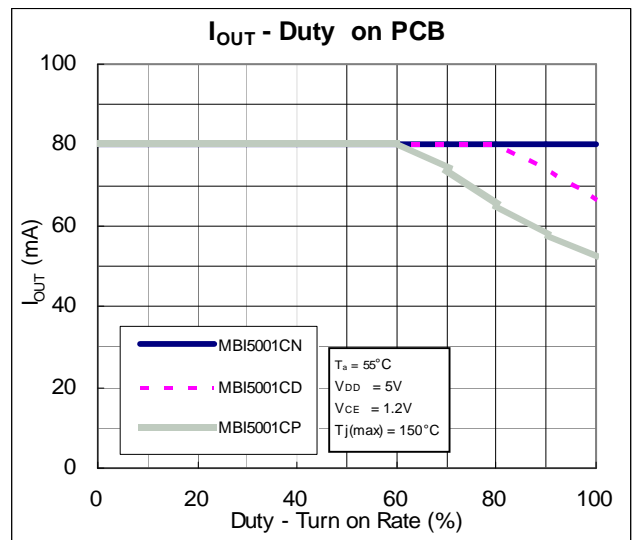
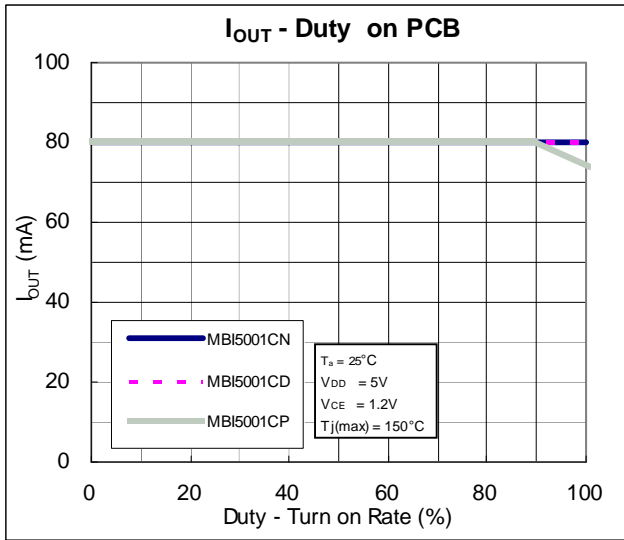


2. /OE, OUTn



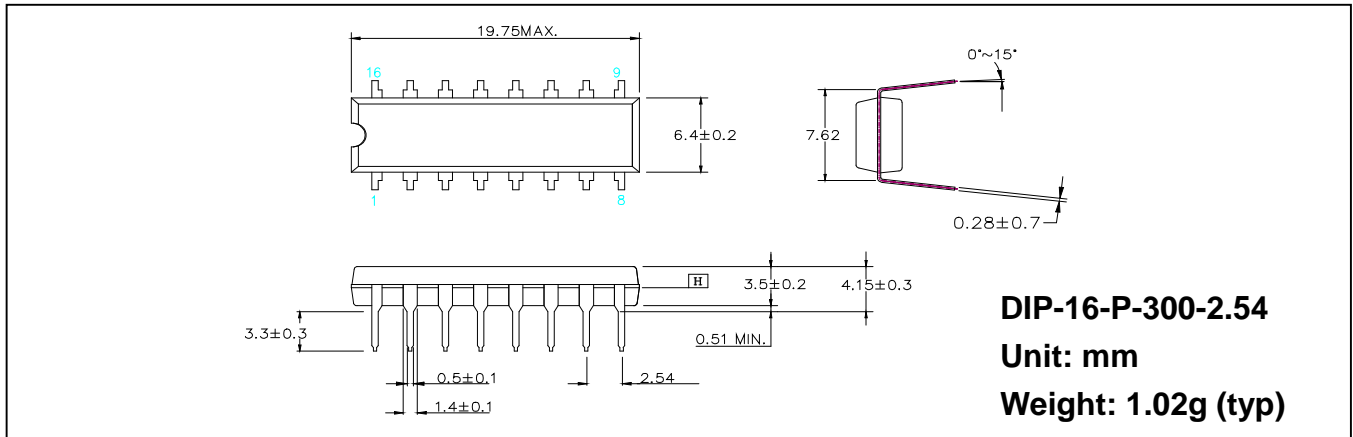


Graphs

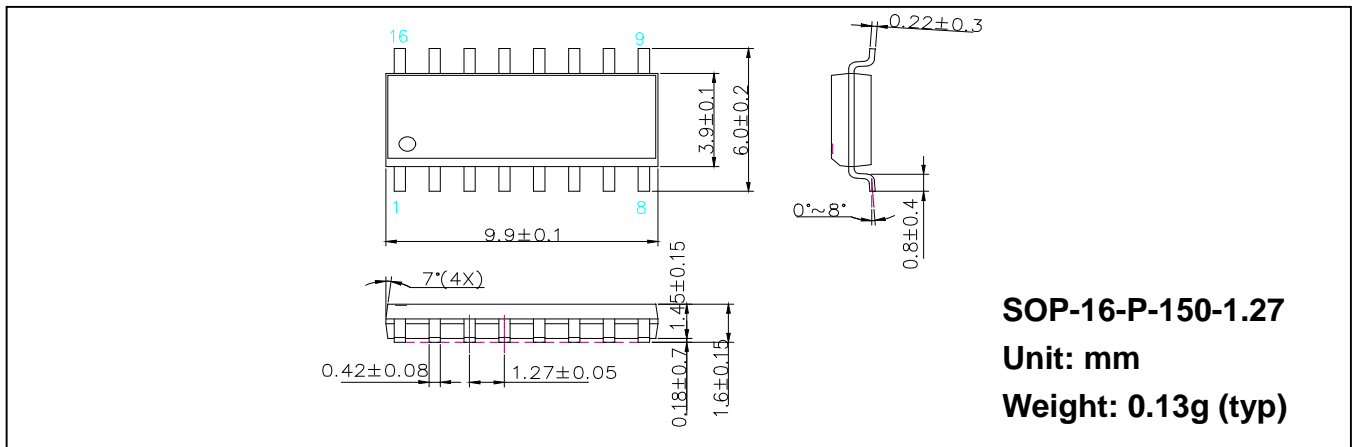


Outline Drawings

MBI5001CN



MBI5001CD



MBI5001CP

