



# STP45NF06L STB45NF06L

N-CHANNEL 60V - 0.022Ω - 38A TO-220 / D<sup>2</sup>PAK  
STripFET™ II POWER MOSFET

TYPE	V <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub>
STP45NF06L	60 V	< 0.028Ω	38 A
STB45NF06L	60 V	< 0.028Ω	38 A

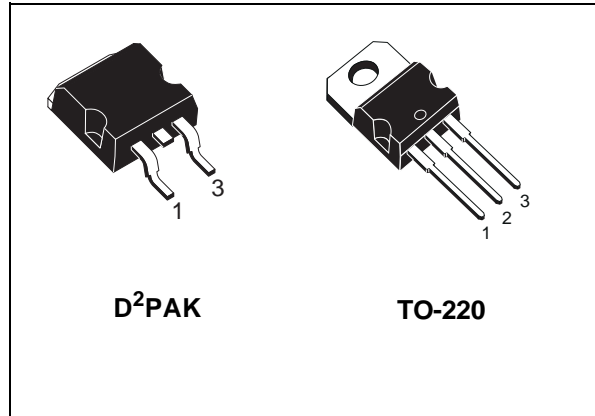
- TYPICAL R<sub>DS(on)</sub> = 0.022Ω
- EXCEPTIONAL dv/dt CAPABILITY
- LOGIC LEVEL GATE DRIVE

## DESCRIPTION

This Power Mosfet is the latest development of STMicroelectronics unique "Single Feature Size™" strip-based process. The resulting transistor shows extremely high packing density for low on-resistance, rugged avalanche characteristics and less critical alignment steps therefore a remarkable manufacturing reproducibility.

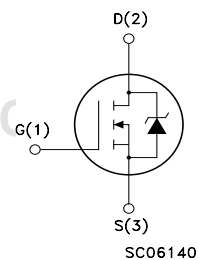
## APPLICATIONS

- HIGH-EFFICIENCY DC-DC CONVERTERS
- SOLENOID AND RELAY DRIVERS
- MOTOR CONTROL, AUDIO AMPLIFIERS
- DC-DC & DC-AC CONVERTERS

D<sup>2</sup>PAK

TO-220

## INTERNAL SCHEMATIC DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V <sub>DS</sub>	Drain-source Voltage (V <sub>GS</sub> = 0)	60	V
V <sub>DGR</sub>	Drain-gate Voltage (R <sub>GS</sub> = 20 kΩ)	60	V
V <sub>GS</sub>	Gate- source Voltage	±16	V
I <sub>D</sub>	Drain Current (continuous) at T <sub>C</sub> = 25°C	38	A
I <sub>D</sub>	Drain Current (continuous) at T <sub>C</sub> = 100°C	26	A
I <sub>DM</sub> (●)	Drain Current (pulsed)	152	A
P <sub>TOT</sub>	Total Dissipation at T <sub>C</sub> = 25°C	80	W
	Derating Factor	0.53	W/°C
dv/dt (1)	Peak Diode Recovery voltage slope	7	V/ns
T <sub>stg</sub>	Storage Temperature	-55 to 175	°C
T <sub>j</sub>	Max. Operating Junction Temperature		

(●) Pulse width limited by safe operating area

(1) I<sub>SD</sub> ≤ 38A, di/dt ≤ 300A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>j</sub> ≤ T<sub>JMAX</sub>.

**STP45NF06L - STB45NF06L****THERMAL DATA**

Rthj-case	Thermal Resistance Junction-case Max	1.87	°C/W
Rthj-amb	Thermal Resistance Junction-ambient Max	62.5	°C/W
T <sub>l</sub>	Maximum Lead Temperature For Soldering Purpose	300	°C

**AVALANCHE CHARACTERISTICS**

Symbol	Parameter	Max Value	Unit
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T <sub>j</sub> max)	38	A
E <sub>AS</sub>	Single Pulse Avalanche Energy (starting T <sub>j</sub> = 25 °C, I <sub>D</sub> = I <sub>AR</sub> , V <sub>DD</sub> = 50 V)	135	mJ

**ELECTRICAL CHARACTERISTICS (TCASE = 25 °C UNLESS OTHERWISE SPECIFIED)**

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source Breakdown Voltage	I <sub>D</sub> = 250 μA, V <sub>GS</sub> = 0	60			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = Max Rating V <sub>DS</sub> = Max Rating, T <sub>C</sub> = 125 °C			1 10	μA μA
I <sub>GSS</sub>	Gate-body Leakage Current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ±16V			±100	nA

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ON (1)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1	1.7	2.5	V
R <sub>DS(on)</sub>	Static Drain-source On Resistance	V <sub>GS</sub> = 5 V, I <sub>D</sub> = 19 A V <sub>GS</sub> = 10V, I <sub>D</sub> = 19 A		0.024 0.022	0.03 0.028	Ω

**DYNAMIC**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g <sub>fs</sub> (1)	Forward Transconductance	V <sub>DS</sub> = 15V, I <sub>D</sub> = 19 A		24		S
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, f = 1 MHz, V <sub>GS</sub> = 0		1600		pF
C <sub>oss</sub>	Output Capacitance			217		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			62		pF

## ELECTRICAL CHARACTERISTICS (CONTINUED)

## SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = 30V, I_D = 19A$		30		ns
$t_r$	Rise Time	$R_G = 4.7\Omega, V_{GS} = 10V$ (see test circuit, Figure 3)		105		ns
$Q_g$	Total Gate Charge	$V_{DD} = 48V, I_D = 38A,$		23	31	nC
$Q_{gs}$	Gate-Source Charge	$V_{GS} = 5V$		7		nC
$Q_{gd}$	Gate-Drain Charge			10		nC

## SWITCHING OFF

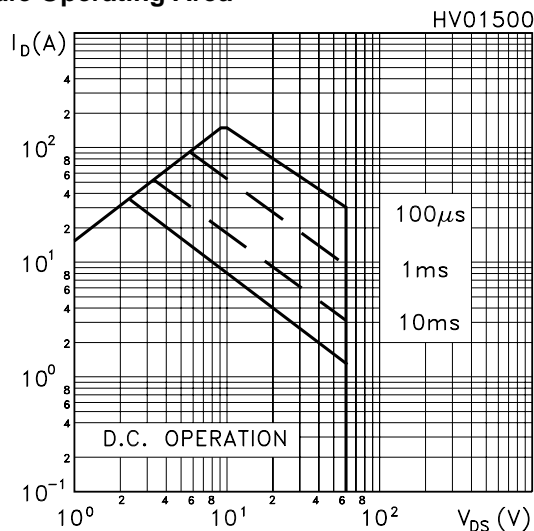
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(off)}$	Turn-off-Delay Time	$V_{DD} = 30V, I_D = 19A,$		65		ns
$t_f$	Fall Time	$R_G = 4.7\Omega, V_{GS} = 10V$ (see test circuit, Figure 3)		25		ns
$t_{d(off)}$	Off-voltage Rise Time	$V_{clamp} = 48V, I_D = 38A$		50		ns
$t_f$	Fall Time	$R_G = 4.7\Omega, V_{GS} = 10V$ (see test circuit, Figure 5)		55		ns
$t_c$	Cross-over Time			85		ns

## SOURCE DRAIN DIODE

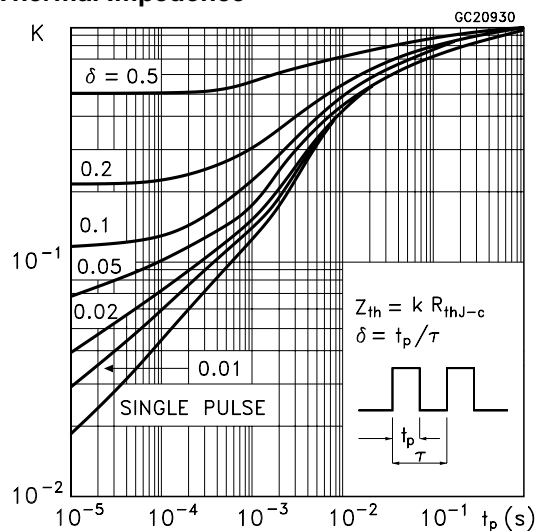
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$I_{SD}$	Source-drain Current				38	A
$I_{SDM(2)}$	Source-drain Current (pulsed)				152	A
$V_{SD(1)}$	Forward On Voltage	$I_{SD} = 38A, V_{GS} = 0$			1.5	V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = 38A, di/dt = 100A/\mu s,$		70		ns
$Q_{rr}$	Reverse Recovery Charge	$V_{DD} = 100V, T_J = 150^\circ C$		110		nC
$I_{RRM}$	Reverse Recovery Current	(see test circuit, Figure 5)		4		A

Note: 1. Pulsed: Pulse duration = 300  $\mu s$ , duty cycle 1.5 %.  
2. Pulse width limited by safe operating area.

## Safe Operating Area

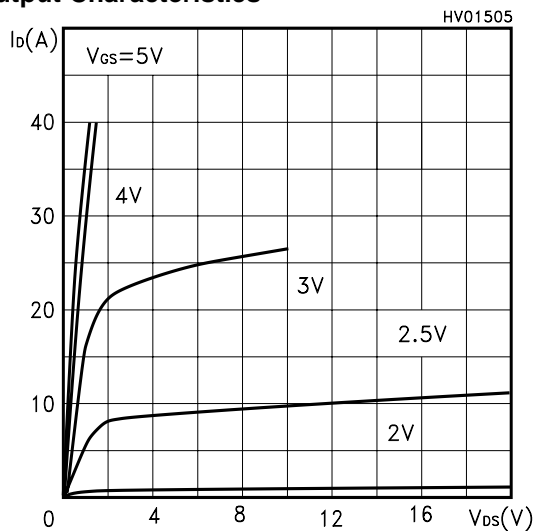


## Thermal Impedence

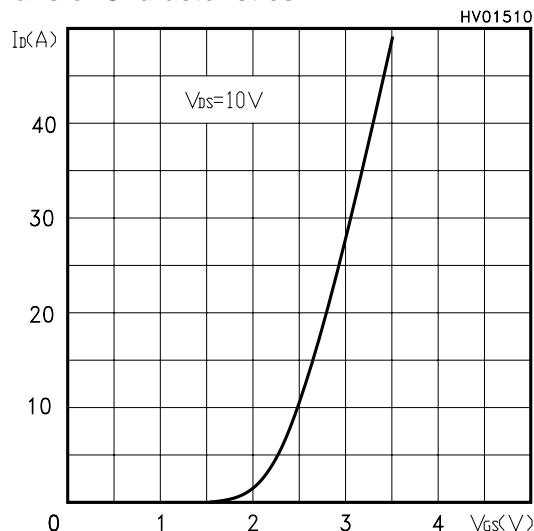


# STP45NF06L - STB45NF06L

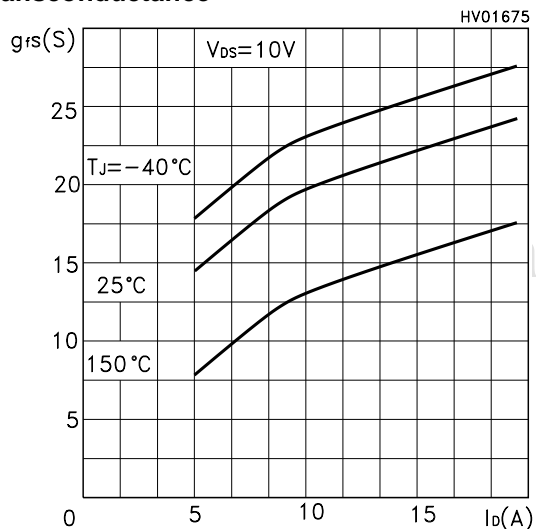
## Output Characteristics



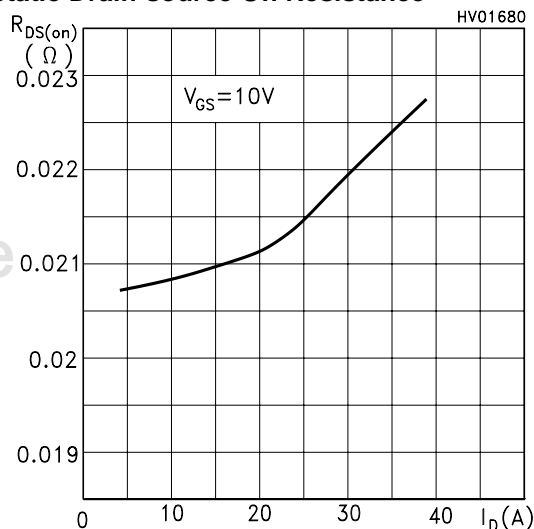
## Transfer Characteristics



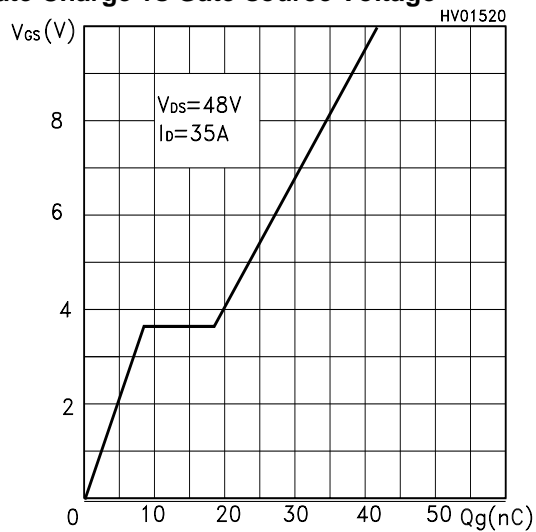
## Transconductance



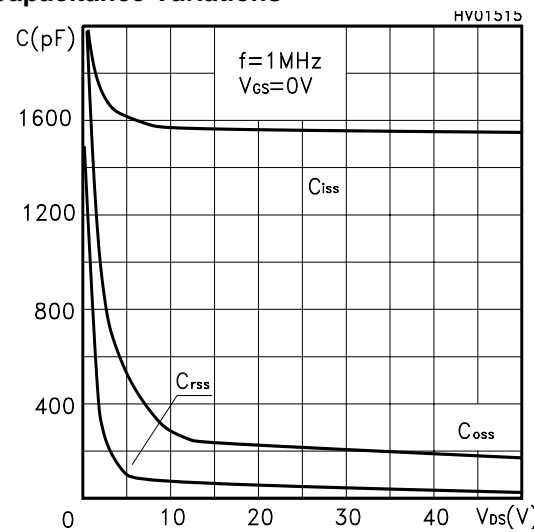
## Static Drain-source On Resistance



## Gate Charge vs Gate-source Voltage

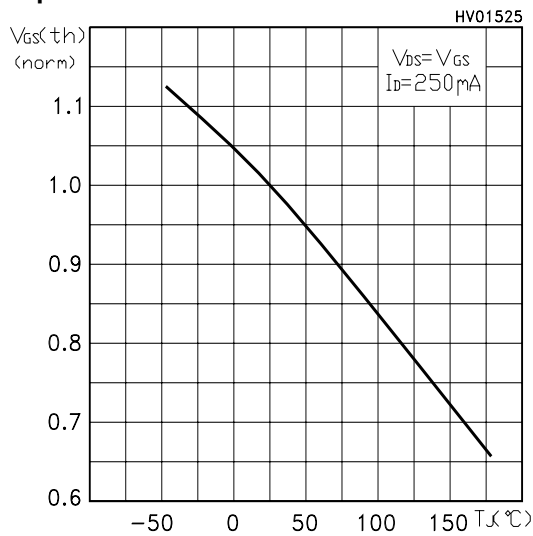


## Capacitance Variations

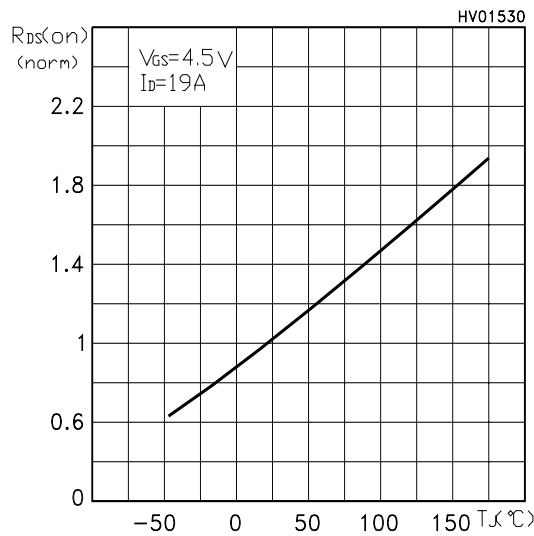


STP45NF06L - STB45NF06L

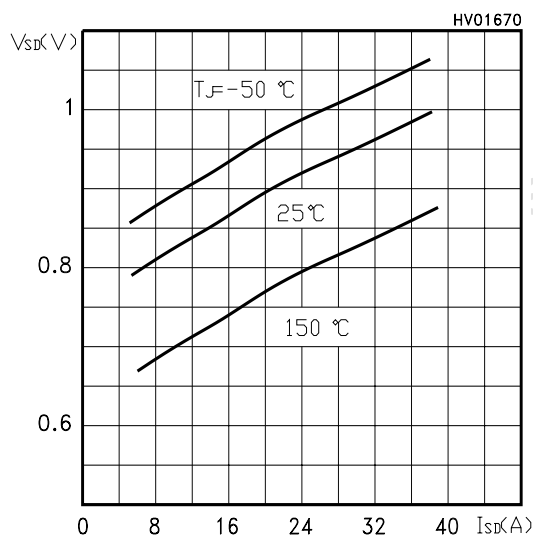
Normalized Gate Threshold Voltage vs Temperature



Normalized On Resistance vs Temperature



Source-drain Diode Forward Characteristics

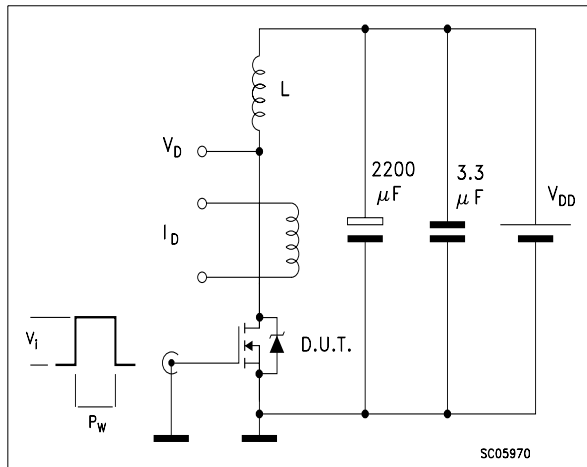


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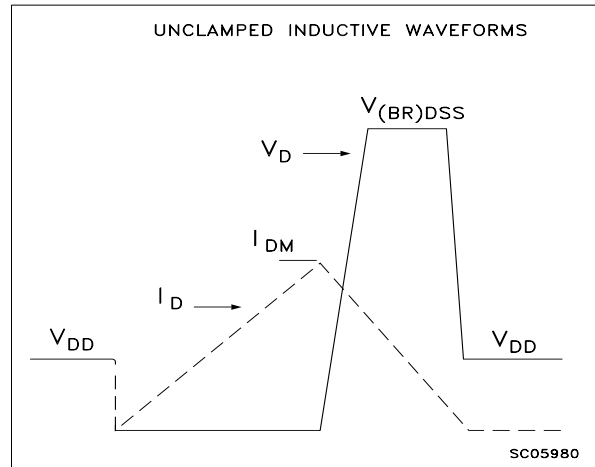


## STP45NF06L - STB45NF06L

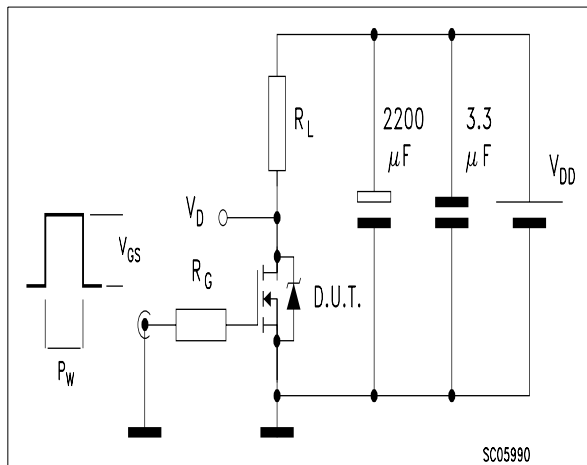
**Fig. 1: Unclamped Inductive Load Test Circuit**



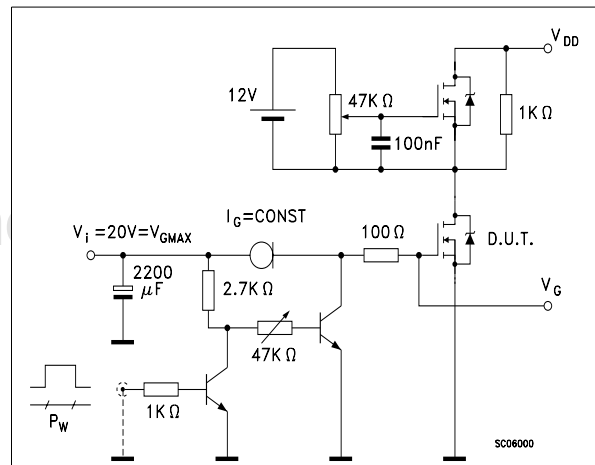
**Fig. 2: Unclamped Inductive Waveform**



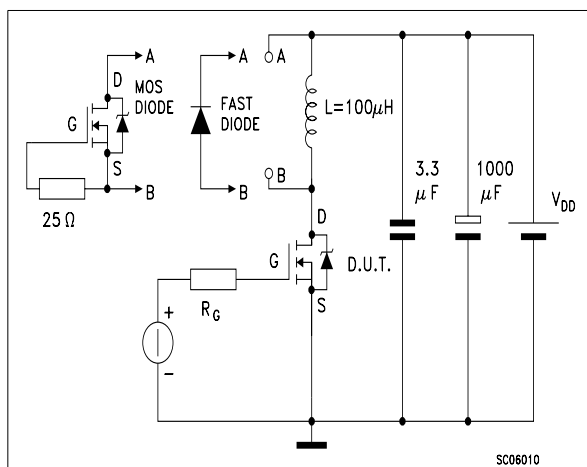
**Fig. 3: Switching Times Test Circuit For Resistive Load**



**Fig. 4: Gate Charge test Circuit**

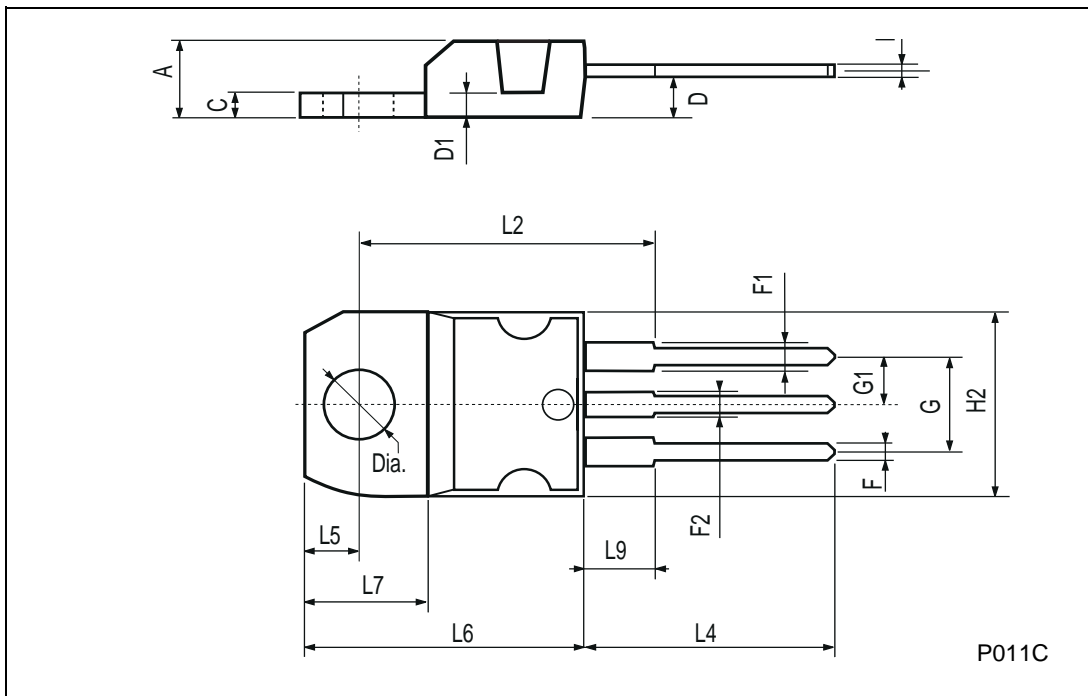


**Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times**



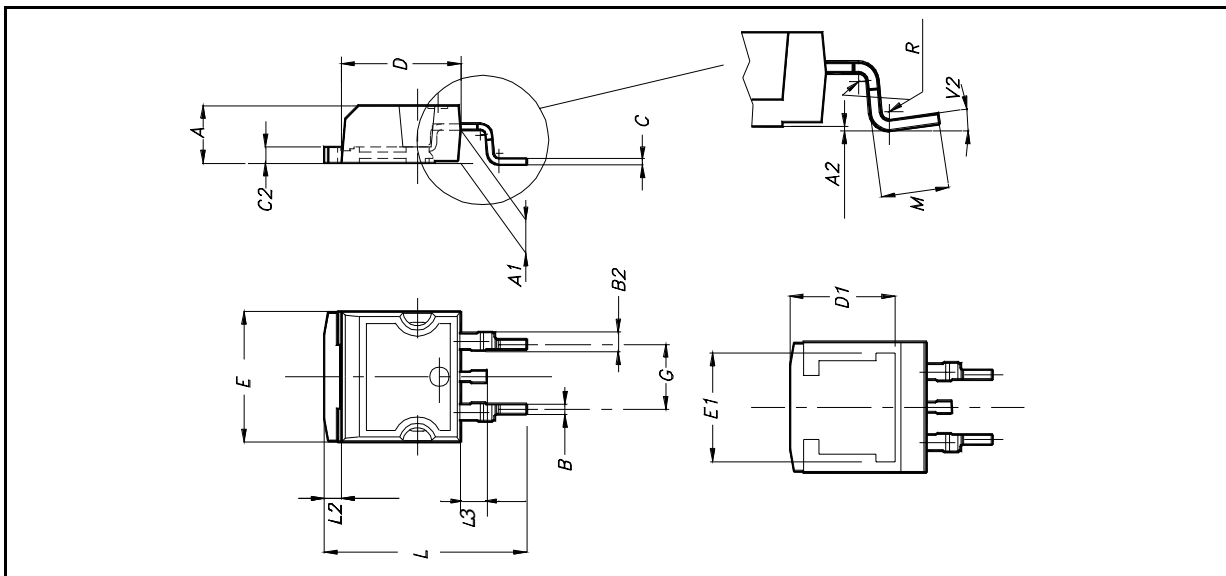
## TO-220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



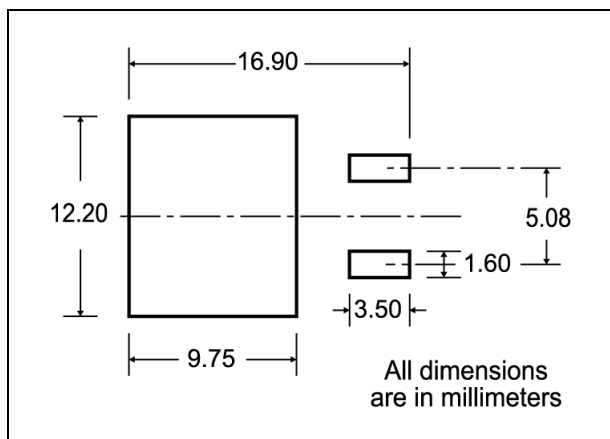
**STP45NF06L - STB45NF06L****D<sup>2</sup>PAK MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
C	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
M	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		8°			

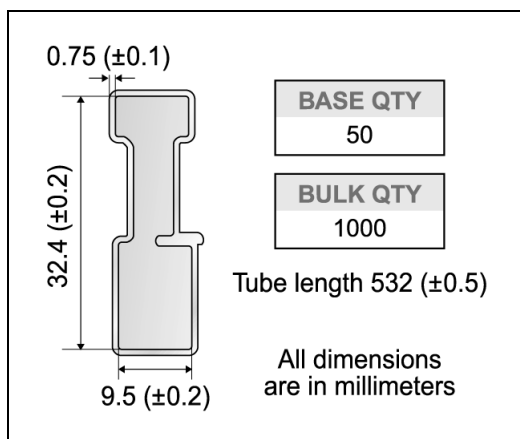




**D<sup>2</sup>PAK FOOTPRINT**



**TUBE SHIPMENT (no suffix)\***



**TAPE AND REEL SHIPMENT (suffix "T4")\***

Diagram showing the tape mechanical data. It includes a circular hub with a diameter of A, a central slot with width D, and a full radius. A tape slot in the core for tape start has a width of 2.5 mm min. An access hole at the slot location has a diameter of 40 mm min. The tape thickness is T, and the hub thickness is C. The distance from the center of the hub to the edge of the tape is N, and the distance from the center of the hub to the center of the slot is G (measured at the hub).

**REEL MECHANICAL DATA**

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A		330		12.992
B	1.5		0.059	
C	12.8	13.2	0.504	0.520
D	20.2		0.795	
G	24.4	26.4	0.960	1.039
N	100		3.937	
T		30.4		1.197

BASE QTY	BULK QTY
1000	1000

**TAPE MECHANICAL DATA**

DIM.	mm		inch	
	MIN.	MAX.	MIN.	MAX.
A0	10.5	10.7	0.413	0.421
B0	15.7	15.9	0.618	0.626
D	1.5	1.6	0.059	0.063
D1	1.59	1.61	0.062	0.063
E	1.65	1.85	0.065	0.073
F	11.4	11.6	0.449	0.456
K0	4.8	5.0	0.189	0.197
P0	3.9	4.1	0.153	0.161
P1	11.9	12.1	0.468	0.476
P2	1.9	2.1	0.075	0.082
R	50		1.574	
T	0.25	0.35	0.0098	0.0137
W	23.7	24.3	0.933	0.956

Diagrams showing the tape and reel shipment details. The top diagram shows a top view of the tape with dimensions K<sub>0</sub>, D, P<sub>2</sub>, P<sub>0</sub>, E, F, W, B<sub>0</sub>, D<sub>1</sub>, A<sub>0</sub>, P<sub>1</sub>, and Center line of cavity. A note indicates a 10 pitches cumulative tolerance on tape of ±0.2 mm. The bottom diagram shows the reel with dimensions TRL, FEED DIRECTION, and Bending radius R min.

\* on sales type



## STP45NF06L - STB45NF06L

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